



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JOHN R ET AL.

Serial No. : 09/915,668

Patent No.: 6,865,695 B2

Issued: Mar. 8, 2005

For: Robust System Bus Recovery

ATTY. DOCKET NO. AUS920000904US1

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Examiner: M. MASKULINKSI

Certificate

APR 04 2006

of Correction

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Assignee respectfully submits the enclosed proposed Certificate of Correction (in duplicate), which corrects a clerical error made by the U.S. Patent and Trademark Office in Letters Patent No. US 6,865,695 B2, issued Mar. 8, 2005.

Respectfully submitted,

*Brian F. Russell*

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ATTORNEY FOR ASSIGNEE

**CERTIFICATE OF MAILING**  
37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First-Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below.

3/28/06  
Date

*Cheryl Kilgus*  
Signature

APR - 4 2006

(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION****PATENT NO : US 6,865,695 B2****DATED : Mar. 8, 2005****INVENTOR(S) : Joyner et al.**

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In Col. 14, (Claim 16), please delete lines 4-6, and insert therefore the following:**

...  
--providing a recovery bus in the high speed system topology  
that operates at a lower speed than the high speed  
interconnect topology;--

**In Col. 14 (Claim 17), please delete lines 13-16 and insert therefore the following:**

--17. The method of claim 16, further including the step of:  
transferring communication from the recovery bus to the  
high speed interconnect topology in response to the  
error being remedied.--

## MAILING ADDRESS OF SENDER:

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PATENT NO. **US 6,865,695 B2**

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
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